









## Computer system with multiple operating system operation

**Patent number:** EP1054322  
**Publication date:** 2000-11-22  
**Inventor:** ENDO YOSHINORI (JP); NAKAMURA KOZO (JP); KAMIWAKI TADASHI (JP); SAITO MASAHIKO (JP); OHNO HIROSHI (JP); NAKAMURA TOMOAKI (JP)  
**Applicant:** HITACHI LTD (JP)  
**Classification:**  
- international: **G06F9/48; G06F9/46;** (IPC1-7): G06F9/46  
- european: G06F9/46C2; G06F9/46C4  
**Application number:** EP20000304271 20000519  
**Priority number(s):** JP19990140914 19990521

### Also published as:

 US6615303 (B1)  
 JP2000330806 (A)  
 EP1054322 (A3)

### Cited documents:

 US4747040  
 WO9912095  
 US4993017  
 EP0360135  
 XP002075223

**Report a data error here**

### Abstract of **EP1054322**

A computer system is provided with a scheme to making the input and output device provided in a computer in common for a plurality of operating system, in a multiple operating system control unit operating a plurality of mutually distinct operating systems on one computer system. The computer system includes a plurality of operating systems, and OS switching unit for switching a plurality of operating systems. The OS switching means makes reference to a preferential interrupt table on the basis of an interrupt factor for switching to corresponding operating system and calls interrupt processing means incorporated in the operating system.

---

Data supplied from the **esp@cenet** database - Worldwide